

AVIO C1605: Avionics Fundamentals 2

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Module Title	e:		Avionics Fundamentals 2
Language o	of Instruction	n:	English
Credits:		10	
NFQ Level:		6	
Module Del	ivered In		2 programme(s)
Teaching & Strategies:	Learning		A series of lectures, tutorials, class-based tasks, and laboratory exercises will be used. The practical sessions will be used to support the theory. The Institute VLE will be used to interactively communicate with students.
Module Aim	1:		To give students an understanding of the principles of avionic circuits. To develop the student's ability to analyse the behaviour of avionic circuits.
Learning O	utcomes		
On successi	ful completior	n of th	is module the learner should be able to:
LO1	Interpret th	e func	damentals of electric and electronic circuits.
LO2	Perform ca	lculati	ions to permit the analysis of both DC and AC circuits.
LO3	Comprehe	nd the	functional operation of common digital electronic devices.
LO4	Work in an	electr	ronic laboratory with due regard for his/her safety and that of others.
LO5	Using sche	ematic	diagrams, build and test electrical and electronic circuits in a laboratory environment.
Pre-requisit	te learning		
	c ommendati learning (or a		tical skill) that is recommended before enrolment in this module.
No recomme	endations liste	ed	
Incompatib These are m		h have	e learning outcomes that are too similar to the learning outcomes of this module.
No incompat	tible modules	s listed	1
Co-requisit	e Modules		
No Co-requi	site modules	listed	
Requireme This is prior		a prac	tical skill) that is mandatory before enrolment in this module is allowed.
No requirem	ents listed		



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Module Content & Assessment

	it					
Generation of Electron of electron						
Magnetism Theory of magneti	sm.					
DC Motor Basic motor theory	/.					
Inductance/Induc Induction principle						
Transformers Transformer const	ruction p	inciples and operation.				
RLC Circuits Phasor analysis of	RLC circ	cuits.				
Filters Operation, applica	tion and i	uses of filters.				
Transistors Transistor character	eristics, p	roperties and applications.				
Integrated Circuit Description and op		f digital logic circuits.				
Assessment Brea	akdown			%		
Continuous Assessment			60.00%			
Practical			40.00%			
Continuous Asse	ssment		•		-	
Assessment Type		Assessment Description	Outcome addressed		% of total	Assessment Date
Other		Several in class and/or online assessments.	1,2,3		60.00	Ongoing
No Project						
Practical						
Assessment	Assess	ment Description	Outcome addressed		% of total	Assessment Date
Туре	Dractio	al Assignments: The student will complete practical nents during the module and write a report on each	1,2,3,4,5		40.00	Every Week

SETU Carlow Campus reserves the right to alter the nature and timings of assessment



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Module Workload

Workload: Full Time		
Workload Type	Frequency	Average Weekly Learner Workload
Lecture	12 Weeks per Stage	5.00
Practicals	12 Weeks per Stage	4.00
Independent Learning	15 Weeks per Stage	9.47
	Total Hours	250.00

Module Delivered In				
Programme Code Programme		Semester	Delivery	
CW_EEAER_B	Bachelor of Engineering (Honours) in Aerospace Engineering	2	Mandatory	
CW_EEACS_D	Bachelor of Engineering in Aircraft Systems	2	Mandatory	